



Law

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2814
Examiner: Phat X. Cao

In Re PATENT APPLICATION OF:

Applicant: KUMAMOTO et al.

Serial No: 10/767,439

Filed: January 30, 2004

For: PROCESS OF PRODUCING
SEMICONDUCTOR CHIP WITH
SURFACE INTERCONNECTION
AT BUMP

Docket No.: AI 318 D1

**RESPONSE TO
NON-COMPLIANT
AMENDMENT**

January 3, 2006

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Responsive to the Notice of Non-Compliant Amendment dated December 20, 2005,
resubmitted with corrections is the objected to section of the Amendment filed December 13,
2005:

FEE ENCLOSED:\$
Please charge any further
fee to our Deposit Account
No. 18-0002